

JAWAHARLAL NEHRU INSTITUTE OF TECHNOLOGY

Mangalpallypatelguda(V),Ibrahimpattanam(M), Ranga Reddy District -501 510.

Telephone No.08414 252214, Fax No.08414 25215.

IV-B.Tech-CSE-A-I-Semester W.E.F.4-7-2011

	I 9:20-10:15	II 10:15-11:10	III 11:10-12:05	IV 12.05-1.00	1.00-1.45	V 1:45-2:40	VI 2:40-3:35	VII 3.35-4:30
MON	NP	WT	SPM	ACA	LUNCH	NP LAB		
TUE	NP	WT	SPM	ACA		WT LAB		
WED	MC	ACA	NP	DWDM		SEMINAR/SOFT SKILLS TRAINING		
THU	MC	ACA	NP	DWDM		SPM	WT	LIBRARY
FRI	SPM	DWDM	MC	WT		ACA	LIBRARY	
SAT	SPM	DWDM	MC	WT				

NP: Ms.Srujini
 SPM: Mr.G.Manjunath
 ACA: Ms.Deepthi Reddy
 DWDM: Ms.Srividya
 WT: Mr.Sunil
 MC: Ms.Navaneetha

PRINCIPAL

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TUE	SPM	ACA	MC	DWDM		WT	LIBRARY	
WED	NP	DWDM	SPM	WT				
THU	NP	DWDM	SPM	WT		NP-LAB		
FRI	MC	WT	NP	ACA		SPM	ACA	LIBRARY
SAT	MC	WT	NP	ACA		WT-LAB		

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III-B.Tech-CSE-A-I-Semester W.E.F.4-7-2011

	I 9:20-10:15	II 10:15-11:10	III 11:10-12:05	IV 12.05-1.00	1.00-1.45	V 1:45-2:40	VI 2:40-3:35	VII 3.35-4:30
MON	PPL	CN & OS LAB			LUNCH	OR	SWE	CN
TUE	MP & I	PPL	OS	CN		SWE	OR	DC
WED	MP & I	PPL	OS	CN		OR	SWE	LIBRARY
THU	MP & I	PPL	SWE	OR		MP & I- LAB		
FRI	MP & I	DC	SWE	OS		SEMINAR		
SAT	OR	PPL	OS	DC				

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MON	OS	SWE	PPL	OR	LUNCH	CN	DC	LIBRARY
TUE	OS	SWE	MP & I	DC		CN	PPL	OR
WED	OS	OR	MP & I	SWE		PPL	CN	DC
THU	SWE	OR	MP & I	PPL		SEMINAR		
FRI	SWE	PPL	MP & I	OR		MP & I-LAB		
SAT	OS	CN & OS LAB						

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B.TECH. II YEAR I SEMESTER (CSE-A) TIME TABLE W.E.F. 04-07-2011

	I 9:20-10:15	II 10:15-11:10	III 11:10-12:05	IV 12:05-1:00	1:00 - 1:40	V 1:45-2:40	VI 2:40-3:35	VII 3:35-4:30
MON	LAB				LUNCH LIME	Electrical Engg	DLD	DSC++
TUE	LAB					DLD	MFCS	P&S (TUT)
WED	MFCS	P&S	EDC	DSC++		DLD	Electrical Engg	DLD (TUT)
THU	MFCS	P&S	EDC	DSC++		Electrical Engg	SEM/LIB	
FRI	MFCS	P&S	EDC	DSC++				
SAT	MFCS	P&S	EDC	DSC++				

ELECTRONIC DEVICES & CIRCUITS (EDC)
MATHEMATICAL FOUNDATION COMPUTER SCIENCE(MFCS)
PROBABILITY & STATISTICS(P&S)
ELECTRICAL ENGINEERING
DATA STRUCTURES THROUGH C++(DSC++)
DIGITAL LOGIC DESIGN(DLD)

:Ashok
:Veena
:Mr.VISWANADH
:
:Tejaswi
:G.L.SINGH

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B.TECH. II YEAR I SEMESTER (CSE-B) TIME TABLE W.E.F. 04-07-2011

	I 9:20-10:15	II 10:15-11:10	III 11:10-12:05	IV 12:05-1:00	1:00 - 1:40	V 1:45-2:40	VI 2:40-3:35	VII 3:35-4:30
MON	EDC	DSC++	MFCS	P&S	LUNCH LIME	DLD	SEM/LIB	
TUE	EDC	DSC++	MFCS	P&S		DLD	MFCS (TUT)	ELECTRICAL ENGG
WED	ELECTRICAL & ELECTRONICS LAB					DLD	MFCS	ELECTRICAL ENGG
THU	DATASTRUCTURES LAB					DLD	MFCS	DSC++ (TUT)
FRI	EDC	DSC++	ELECTRICAL ENGG	P&S		DLD	MFCS	P&S (TUT)
SAT	EDC	DSC++	ELECTRICAL ENGG	P&S				

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DATA STRUCTURES THROUGH C++(DSC++)
DIGITAL LOGIC DESIGN (DLD)
DATA STRUCTURES THROUGH C++ LAB

:Ashok
:
:VISWANADH
:
:Tejaswi
:G.L.SINGH
:TEJASWI

ELECTRICAL ENGINEERING LAB

:

PRINCIPAL

MAVE